

# CSCI 16000 Computer Architecture 1

Instructor: Jaime Canizales

Tuesday - Thursday, 7:00 - 8:15pm Thomas Hunter Building Room 412, Fall 2019

**Text:** Digital Design With An Introduction to the Verilog HDL, 5th Edition. Mano, Ciletti. 2012. ISBN-10: 0132774208 ISBN-13: 978-0132774208

**Email:** jaime.canizales97@myhunter.cuny.edu

**Office Hours:** By appointment.

## Course Description:

Computer Architecture 1 will cover binary and other non-decimal number systems, boolean algebra, combinational circuits, sequential circuits, and how all of these are used in computer hardware. The basics of this class, in particular binary and boolean algebra, are ubiquitous in computer science, and every computer professional should be fully fluent in them.

## Grading:

Midterm Exam: 35%

Final Exam: 35%

Homework: 30%

**Exams:** There will be one midterm and a final. The first midterm will cover the first three chapters of the textbook. The final will cover chapters four, five and some of six.

**Homeworks:** There will be one assignment per chapter, for a total of six assignments. Assignments will be posted and collected on Blackboard.

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